

Title: Layout of Ferroelectric Memory
Device
Serial No. Unassigned
First Named Inventor: Hee Bok KANG
Docket No. 40296-0011

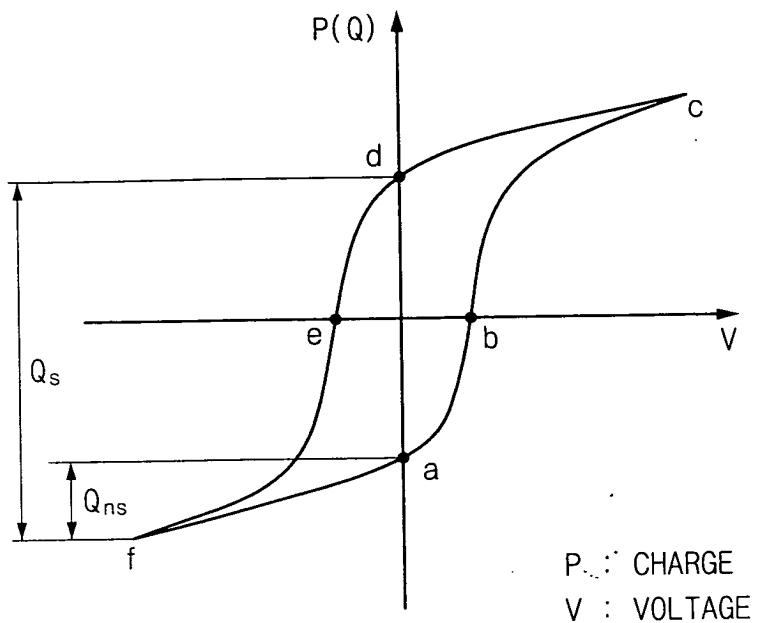


Fig.1
(Prior Art)

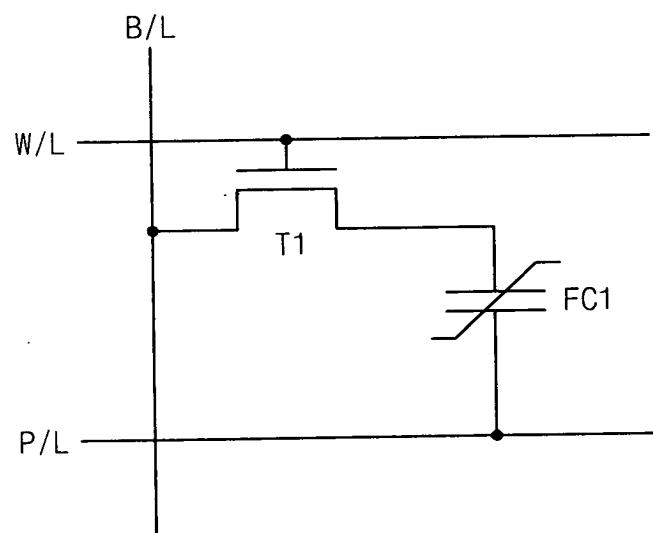


Fig.2
(Prior Art)

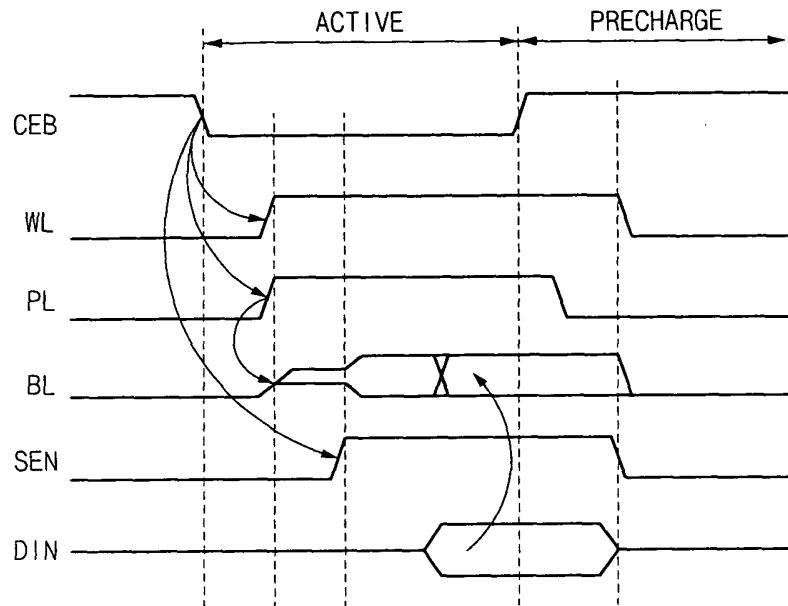


Fig.3A
(Prior Art)

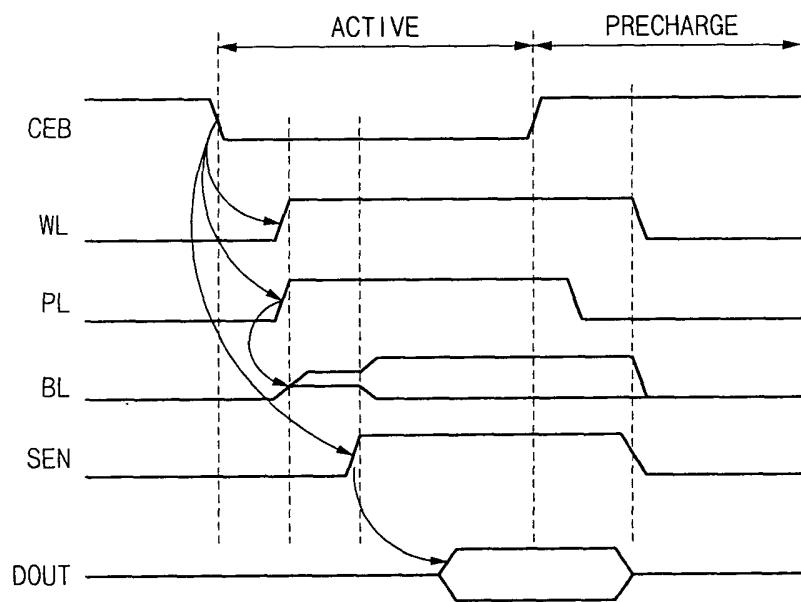


Fig.3B
(Prior Art)

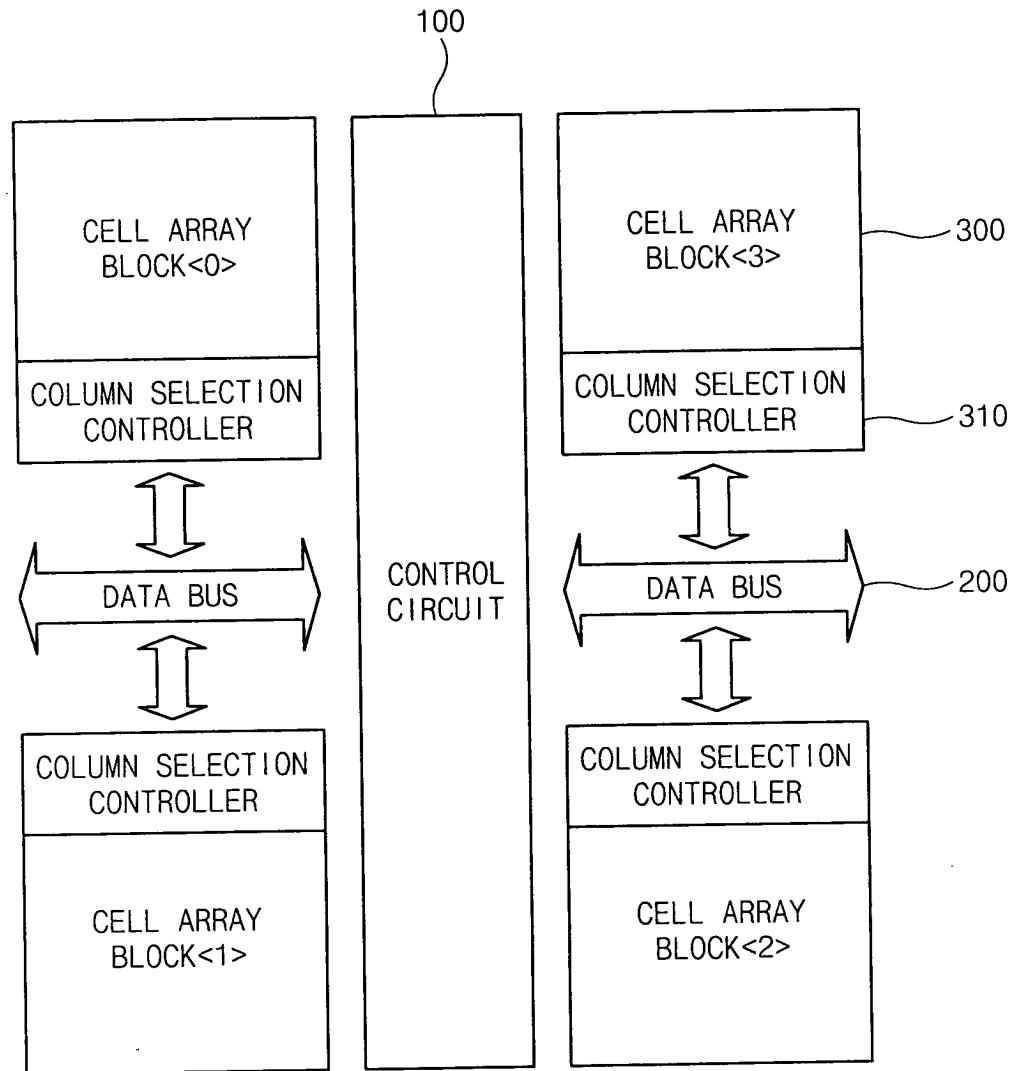


Fig.4

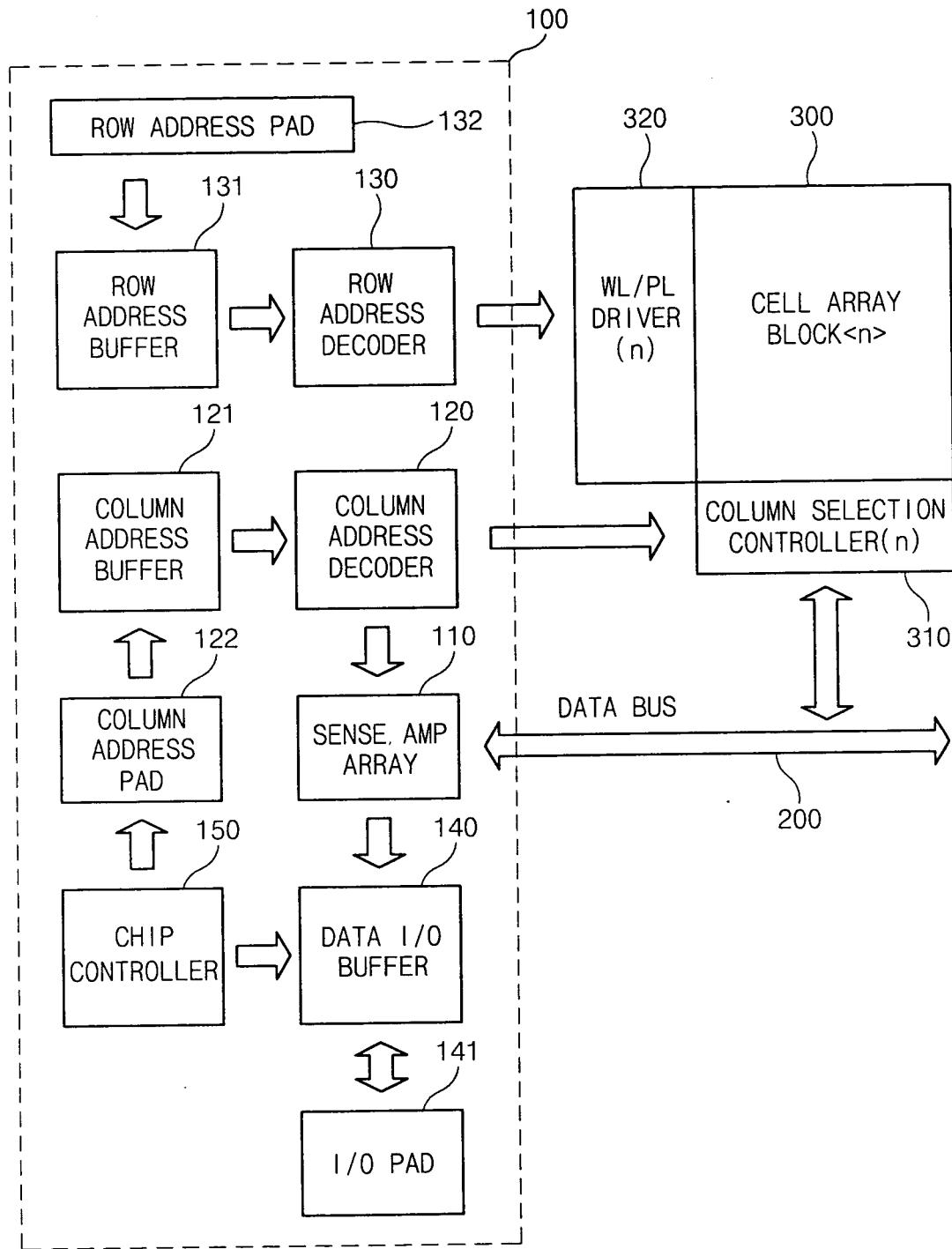


Fig.5

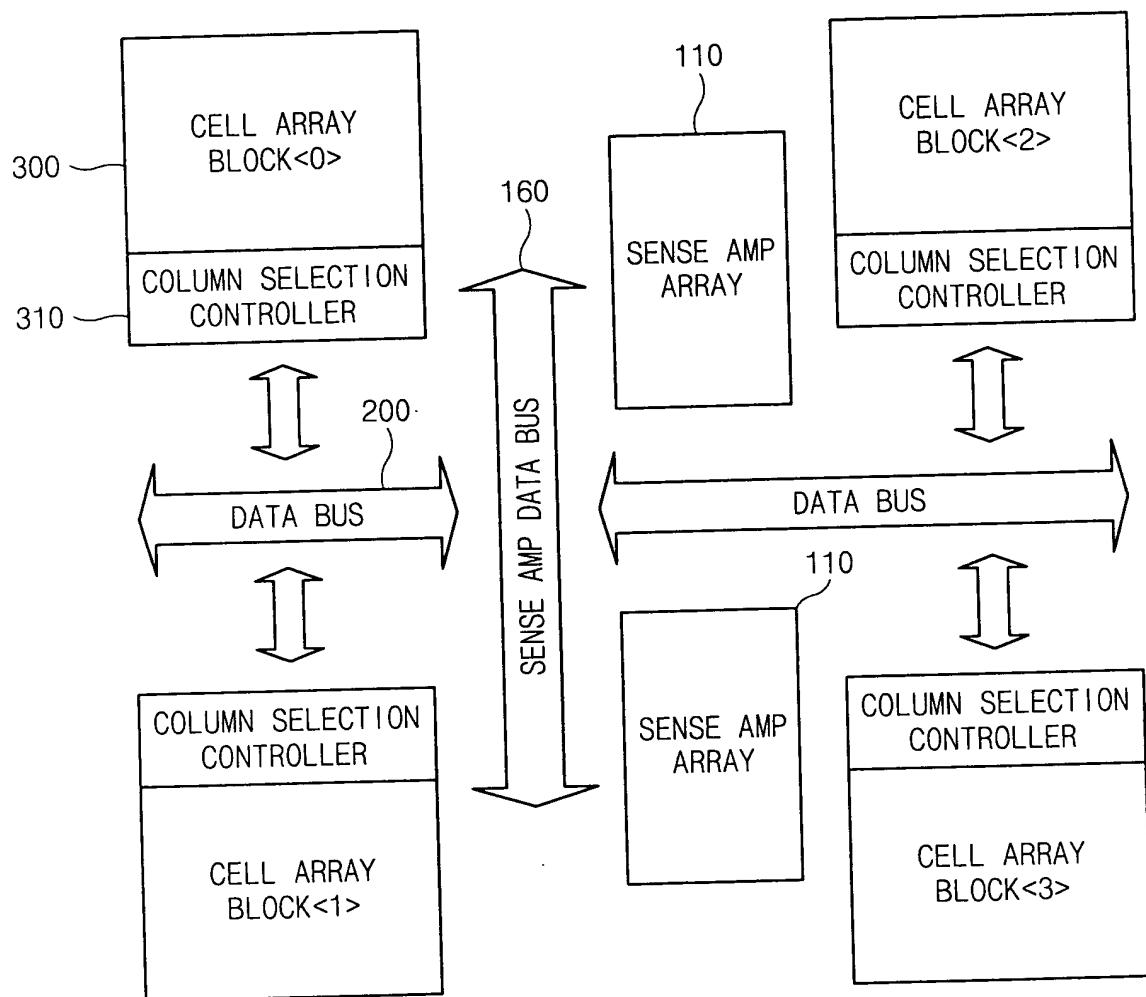


Fig.6

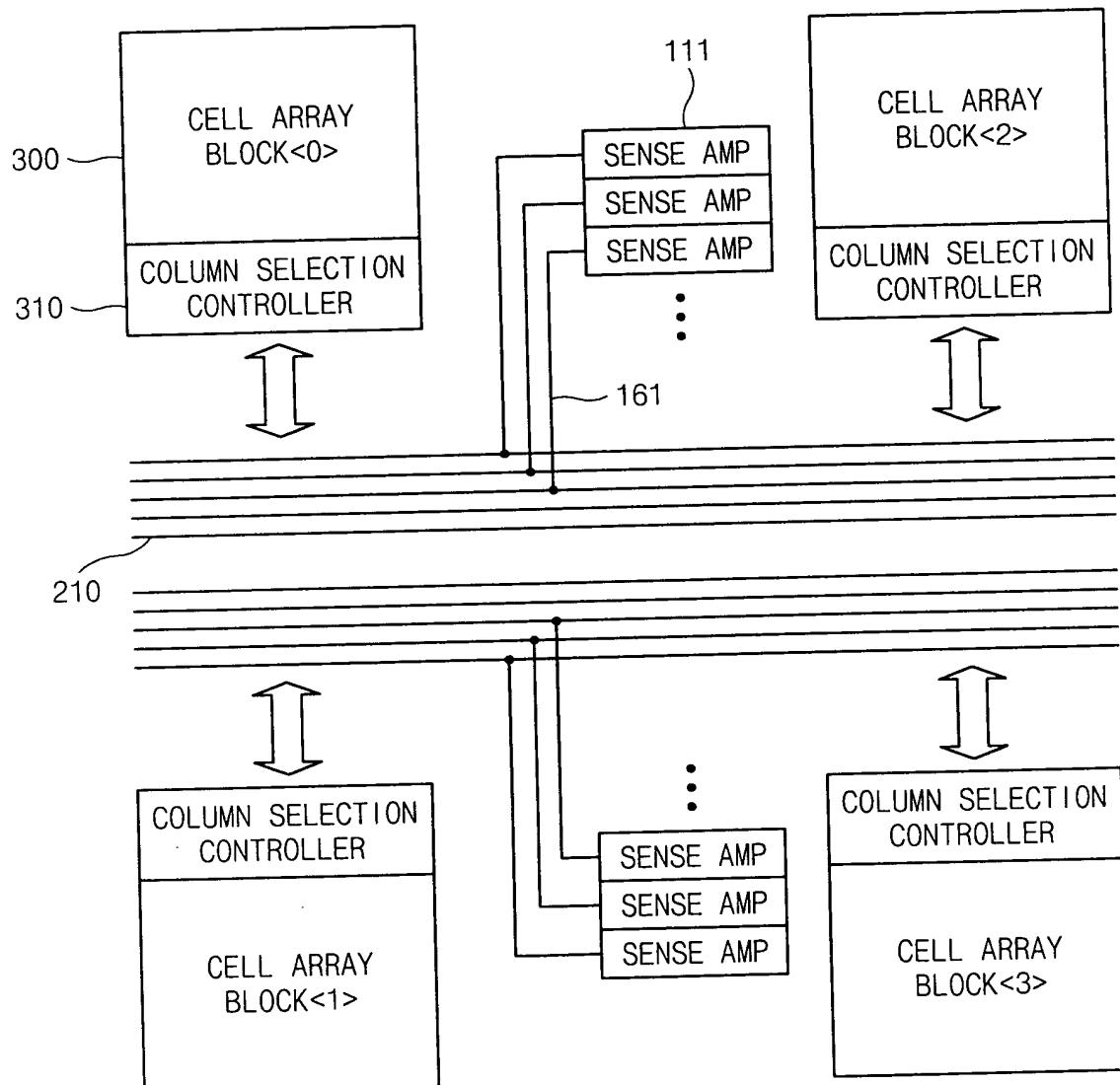


Fig.7

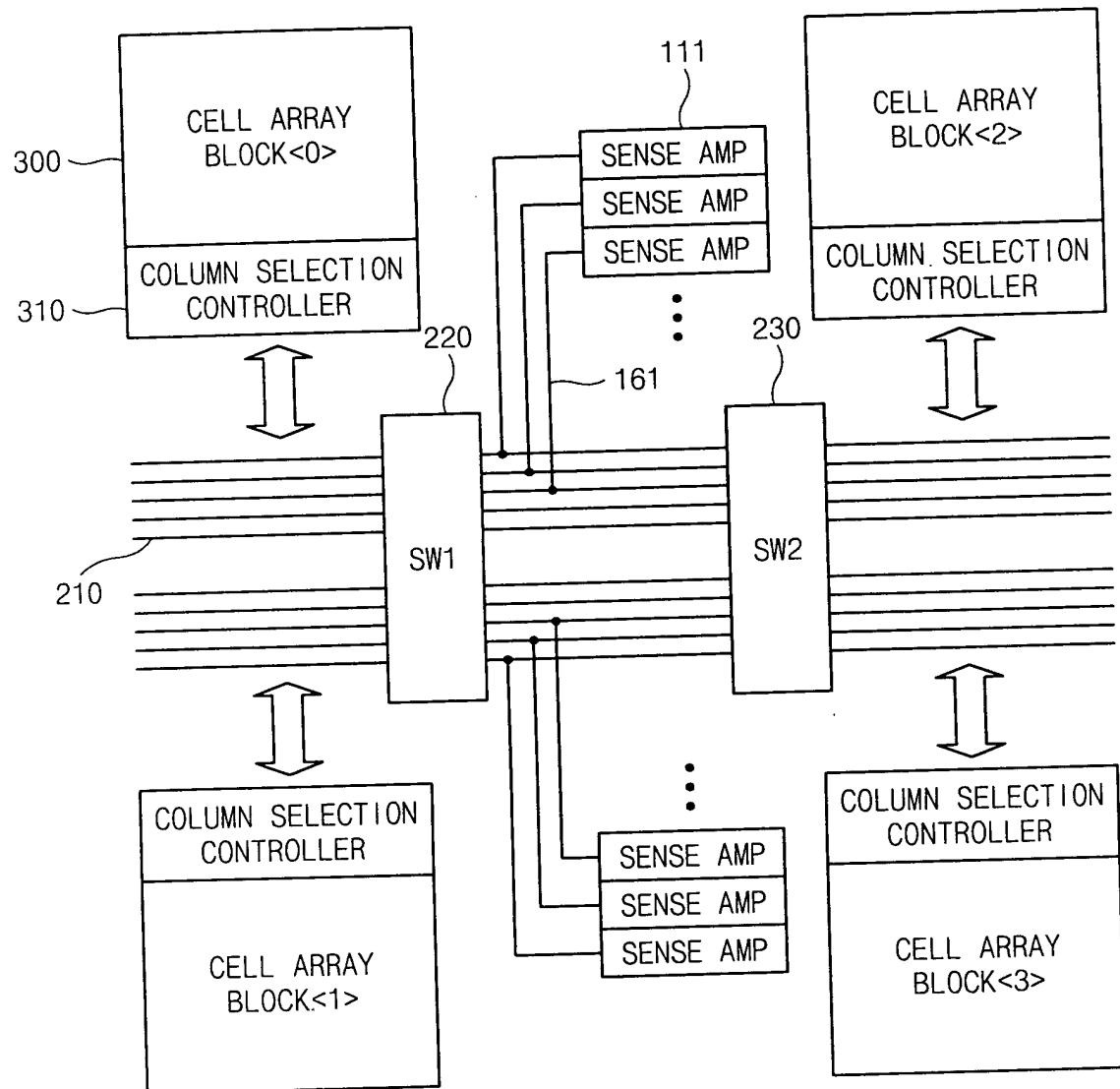


Fig.8A

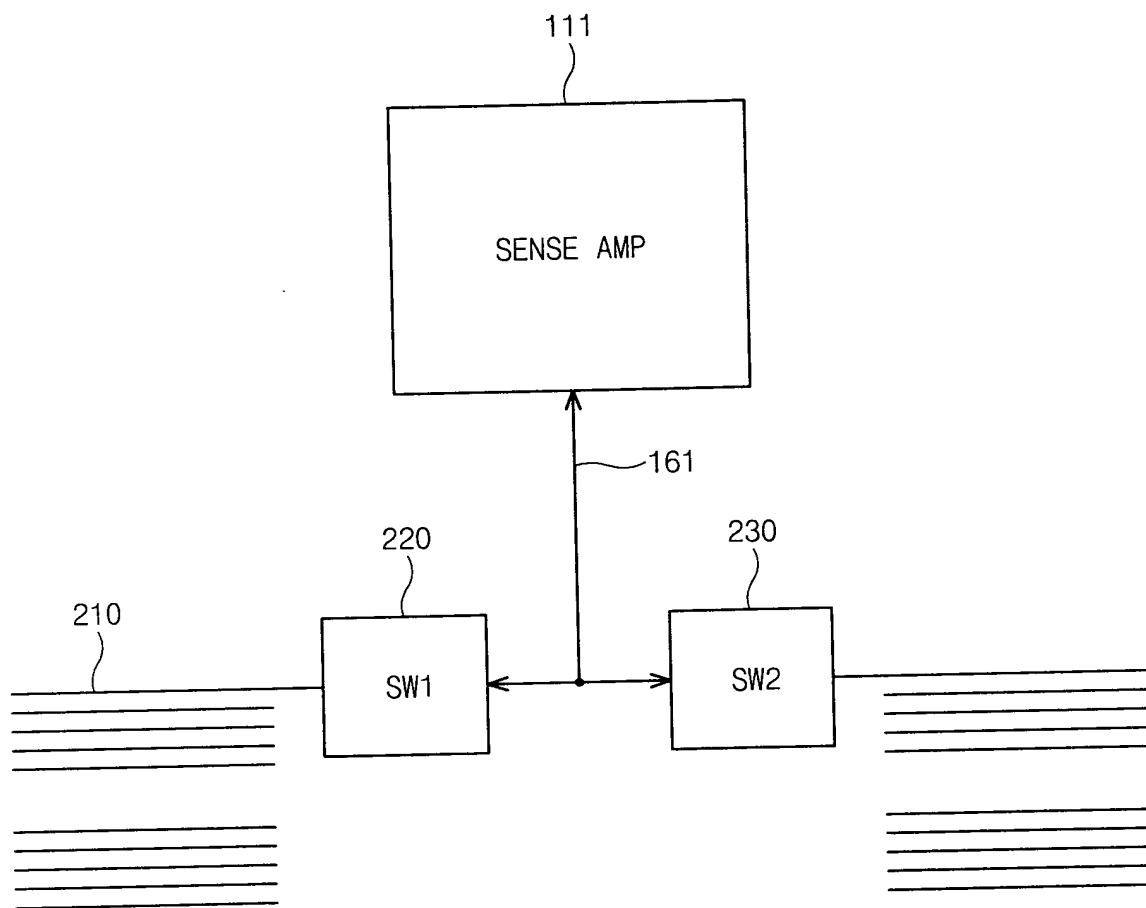


Fig.8B

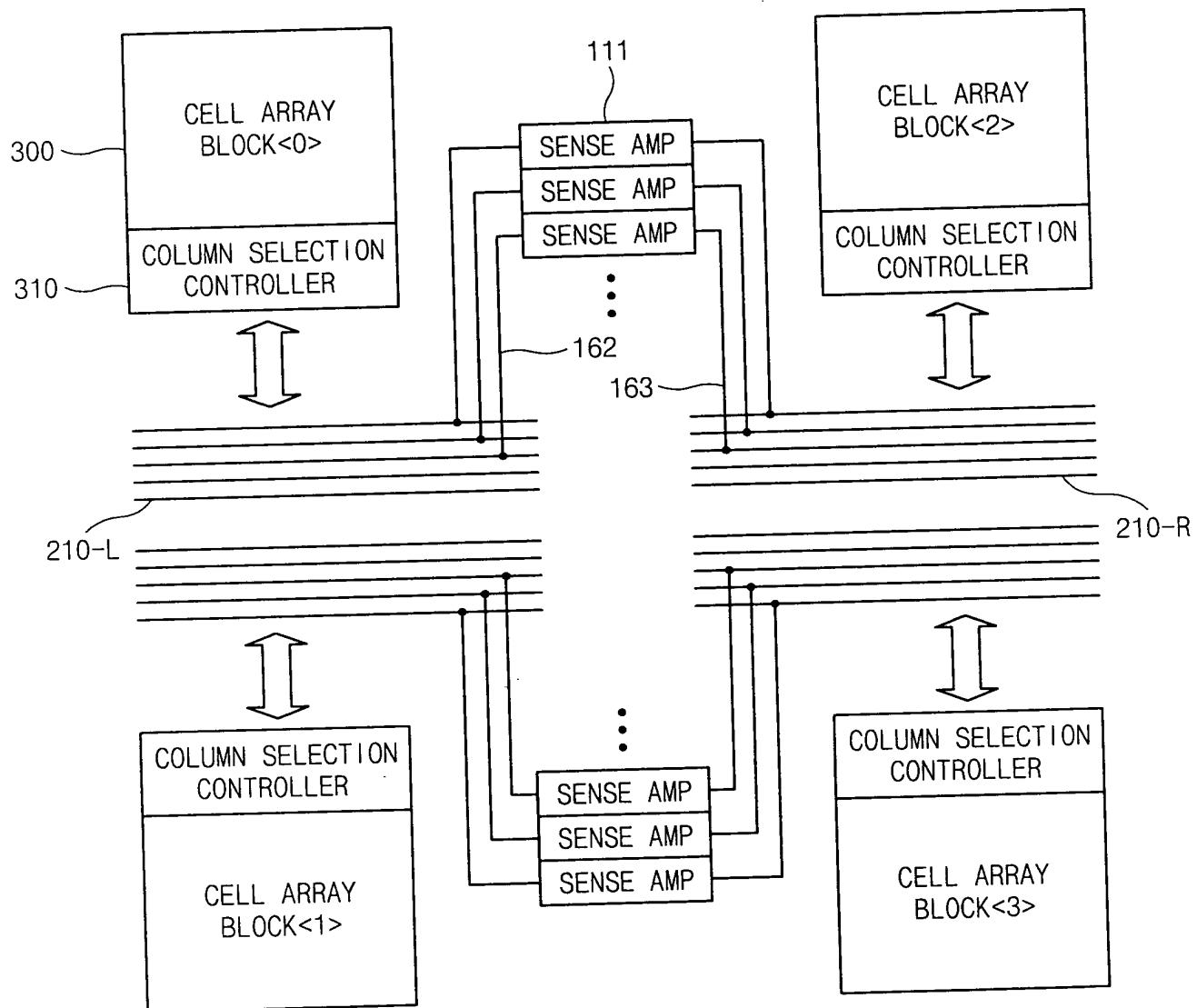


Fig.9A

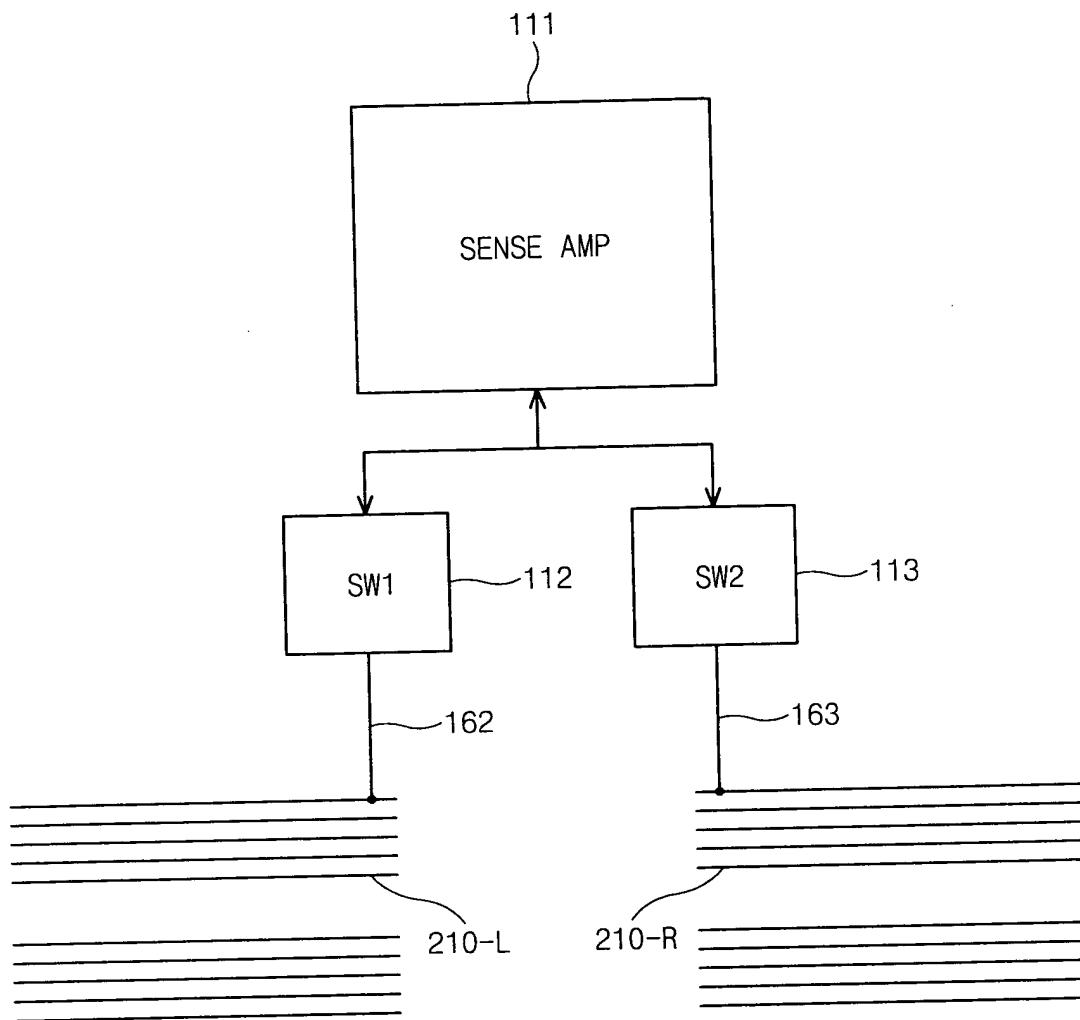


Fig.9B

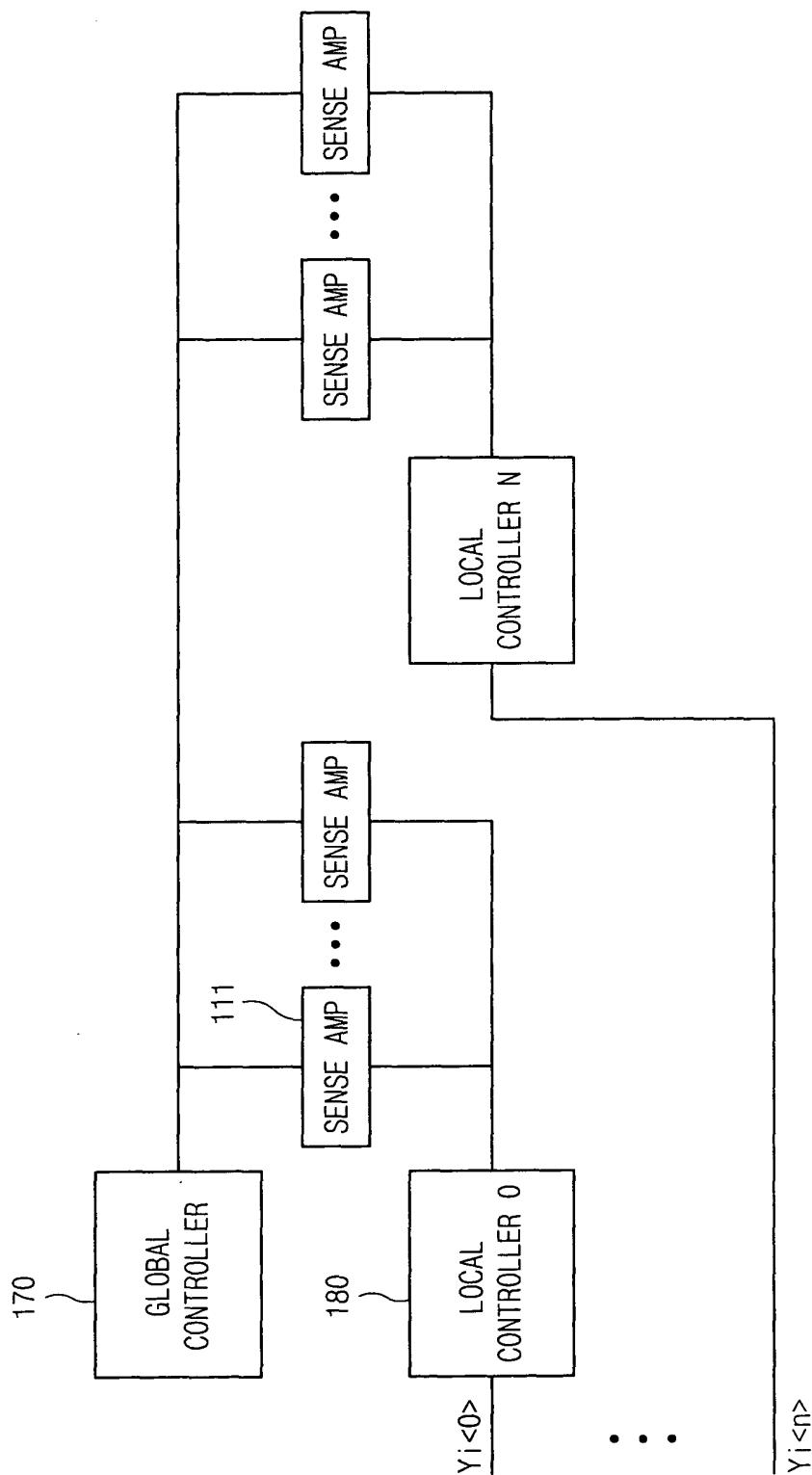


Fig. 10

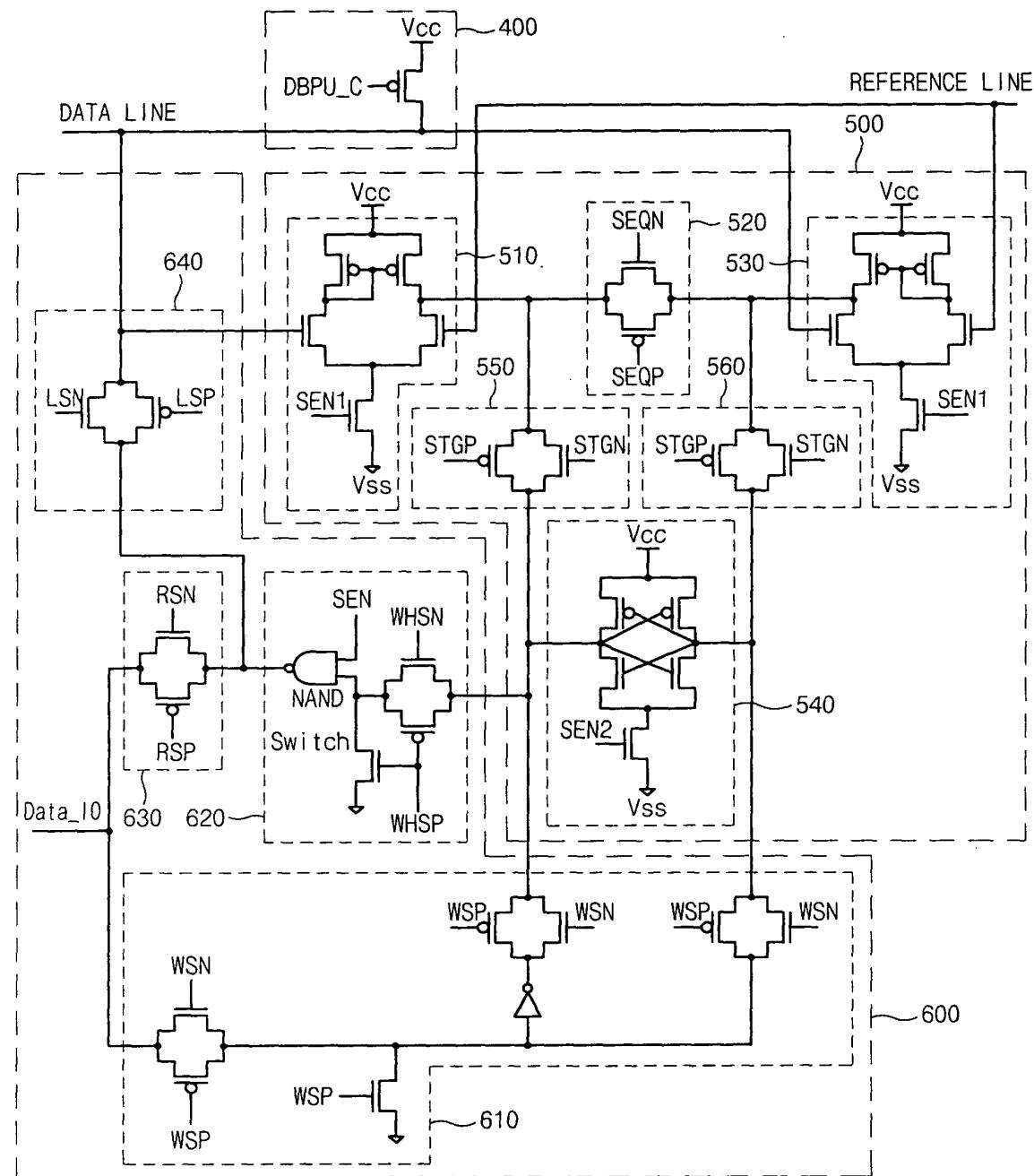


Fig.11A

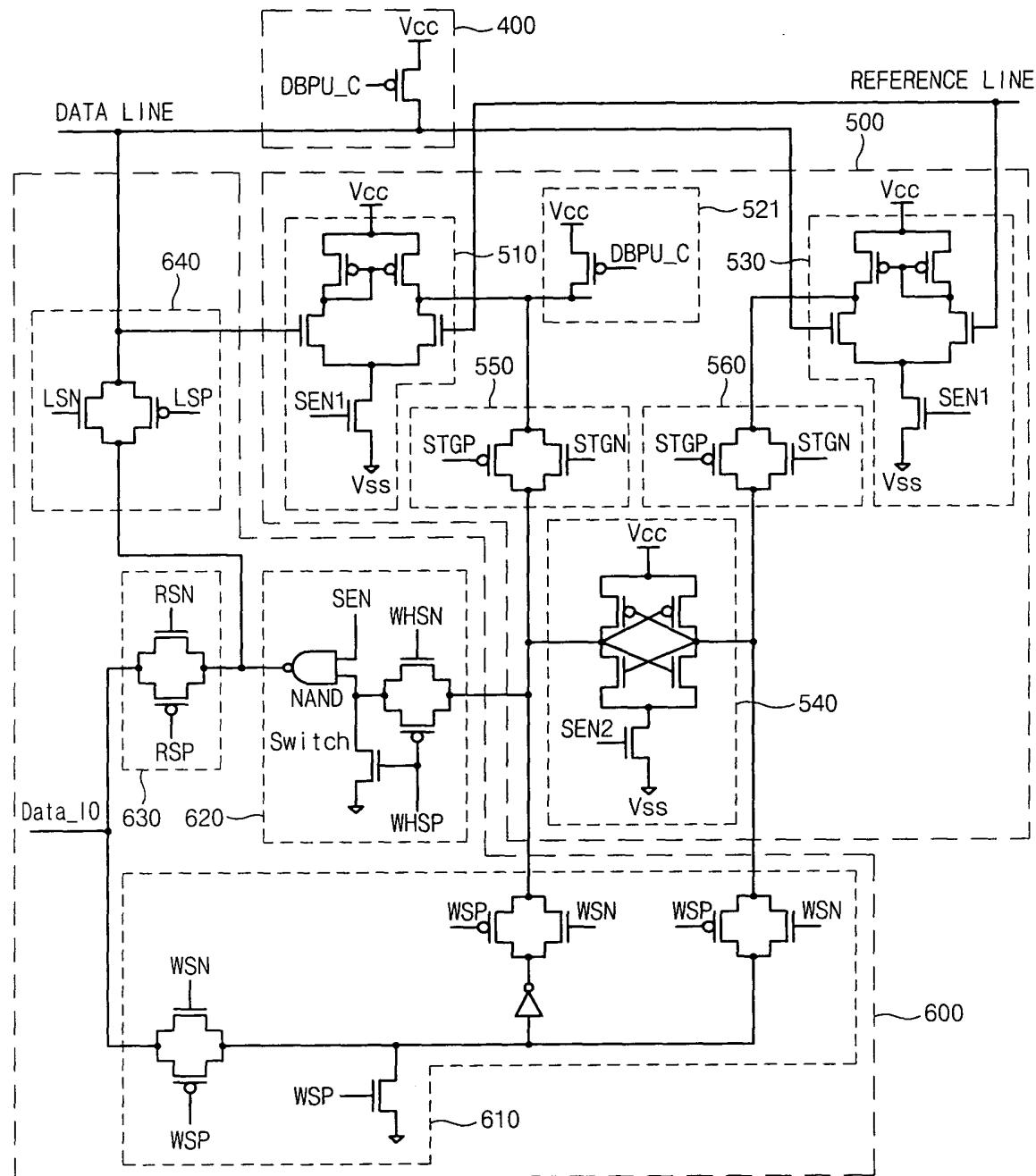


Fig.11B

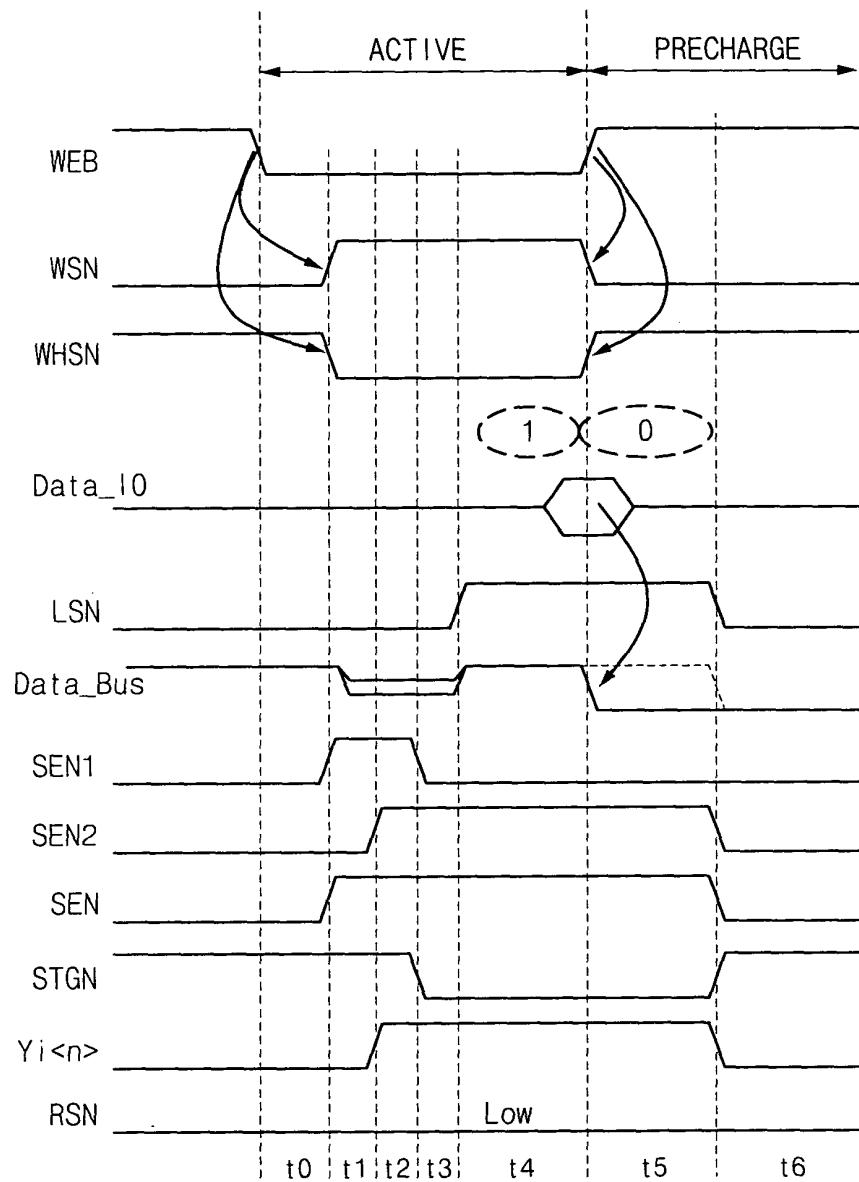


Fig.12

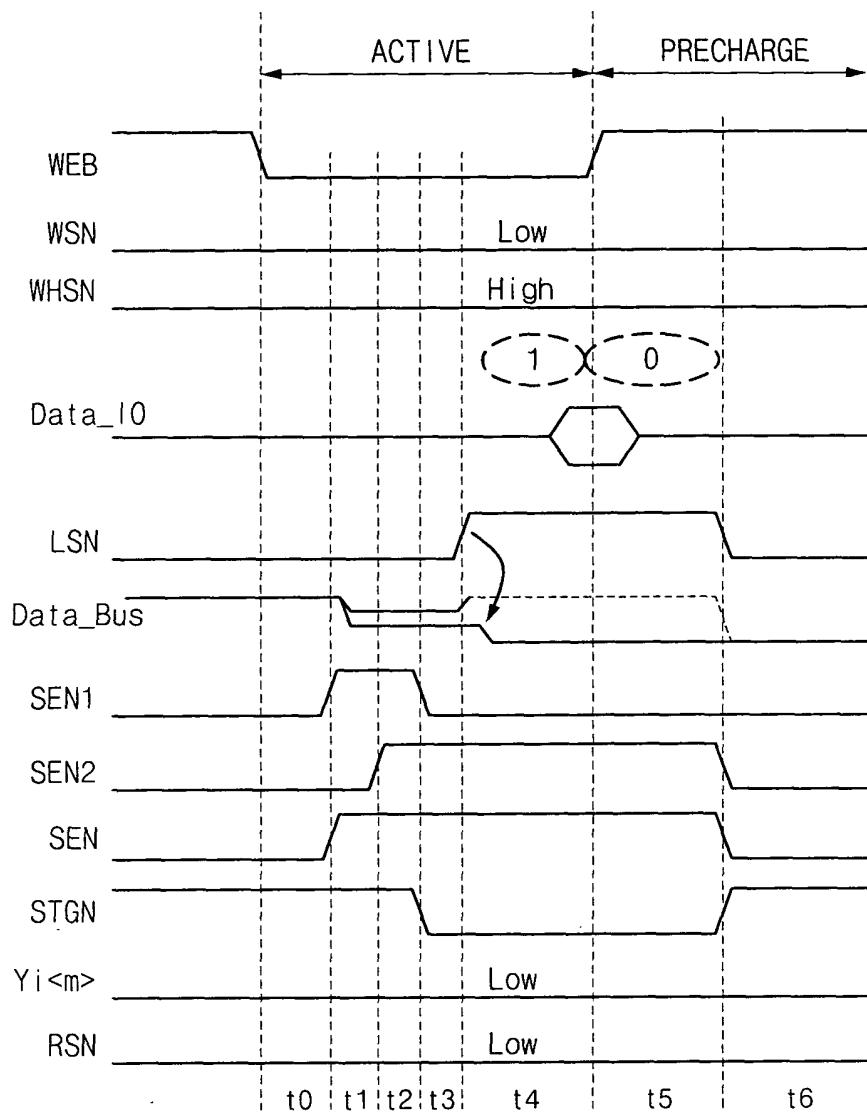


Fig.13

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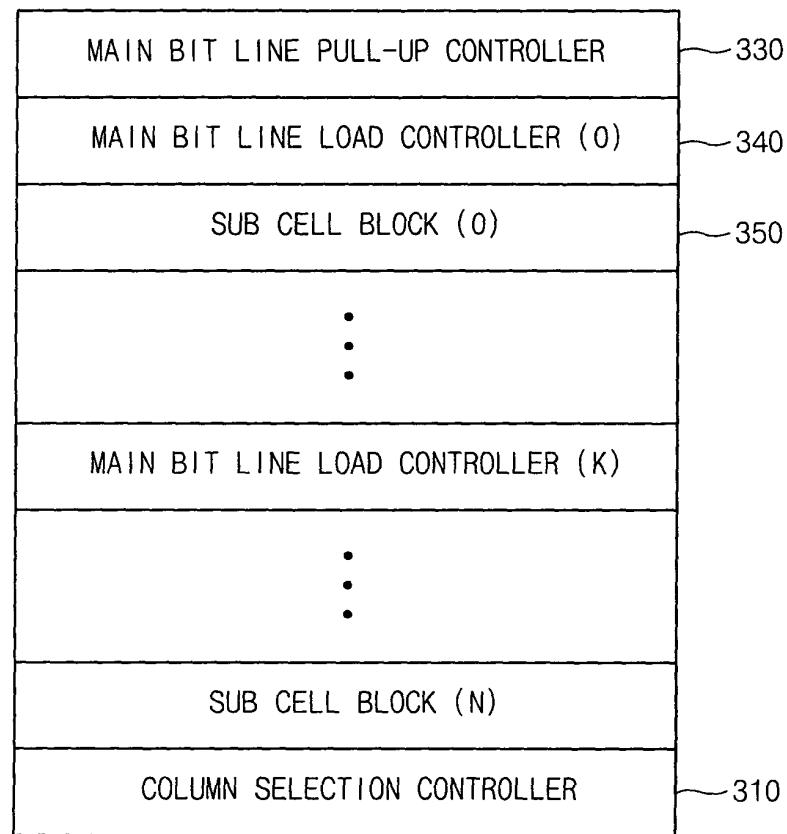


Fig.14

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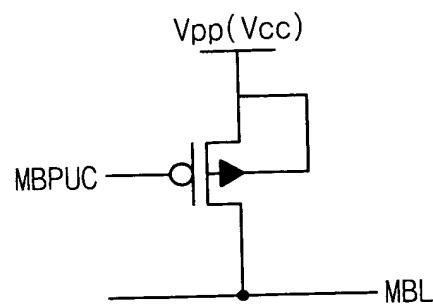


Fig.15

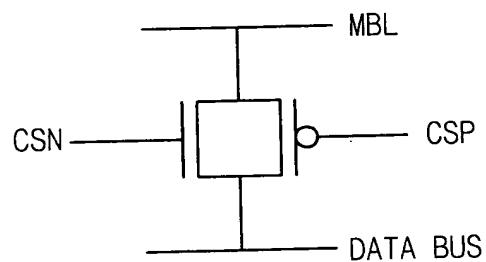


Fig.16

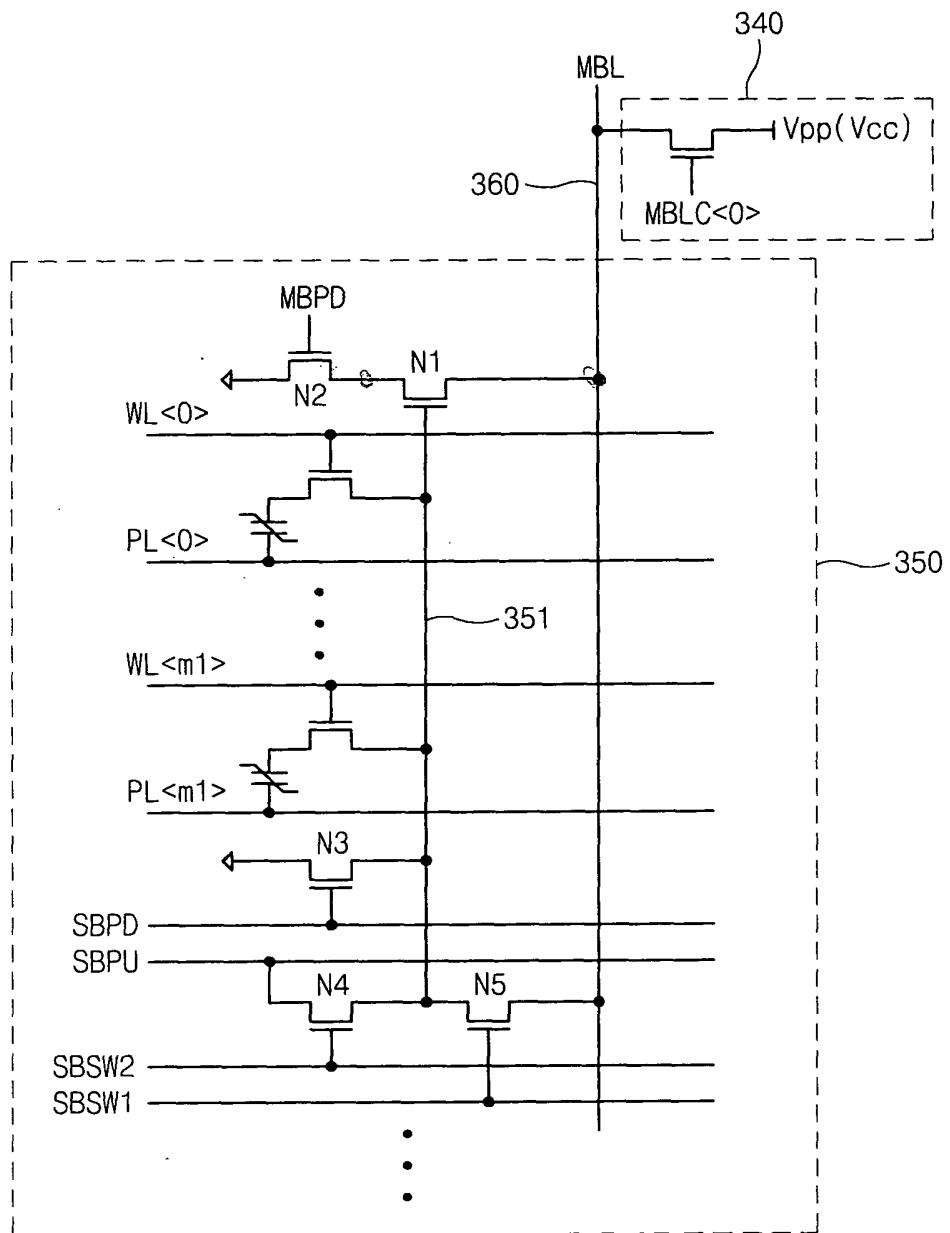


Fig.17

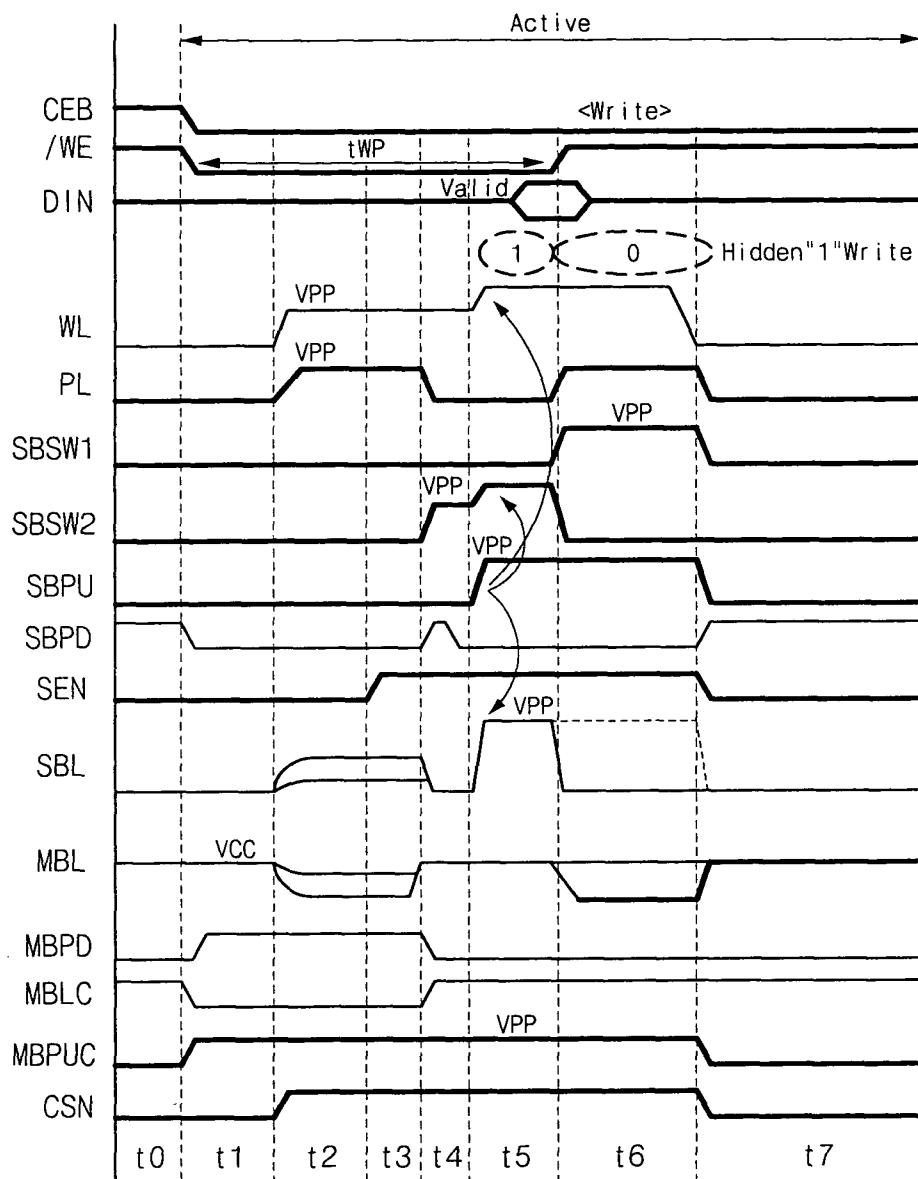


Fig.18A

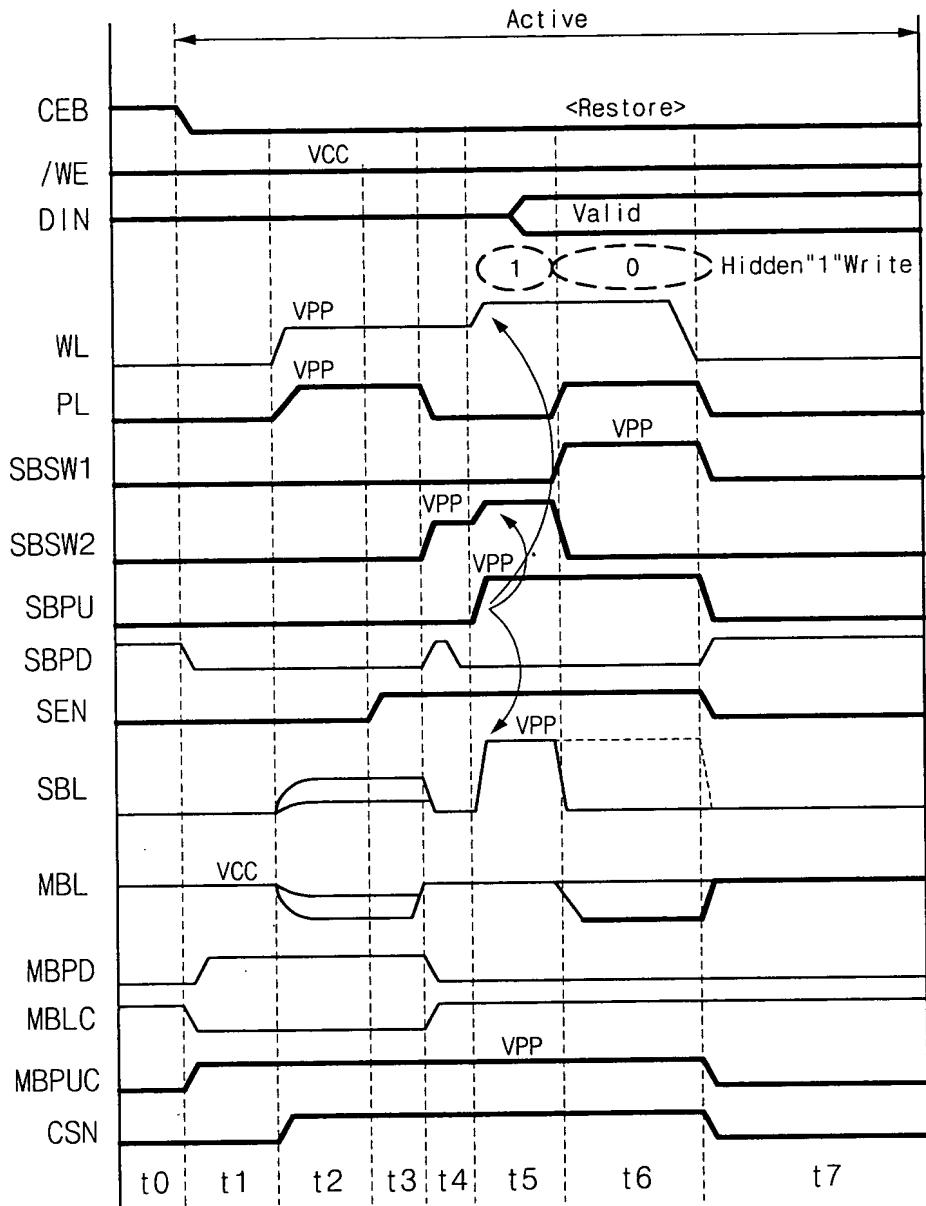


Fig.18B

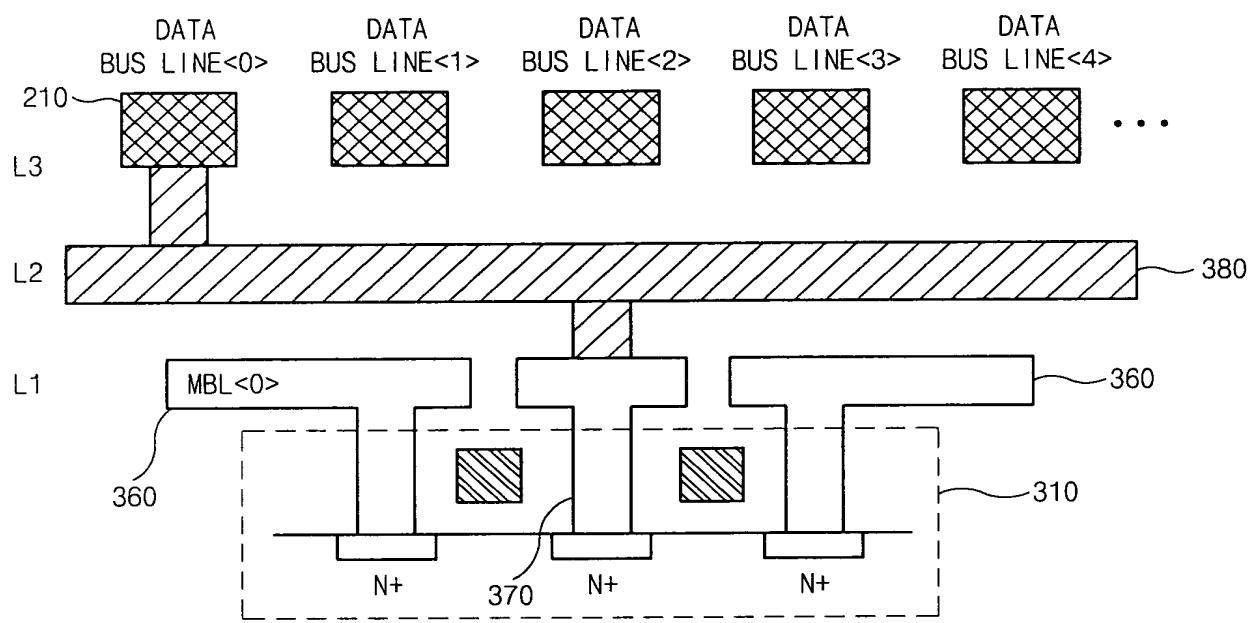


Fig.19

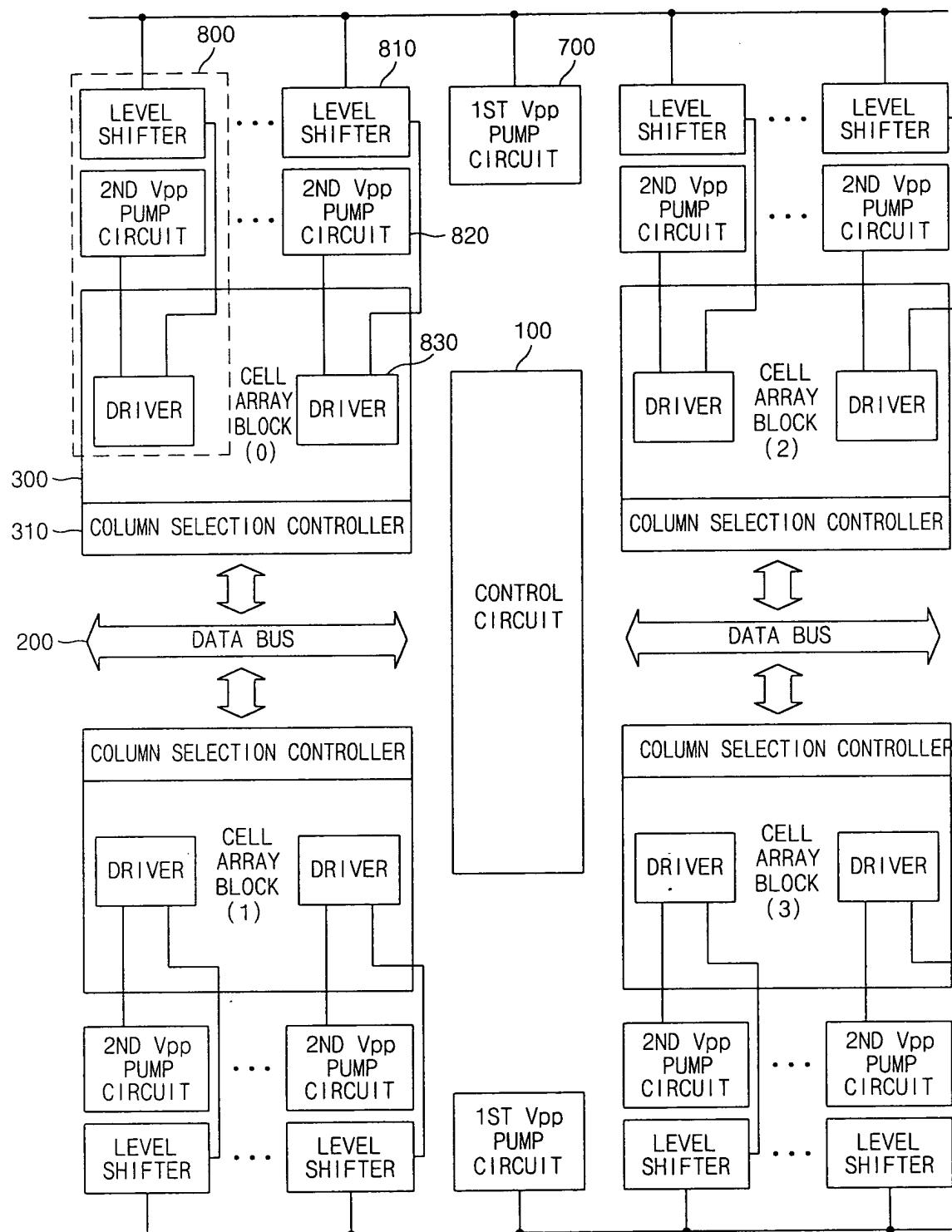


Fig.20

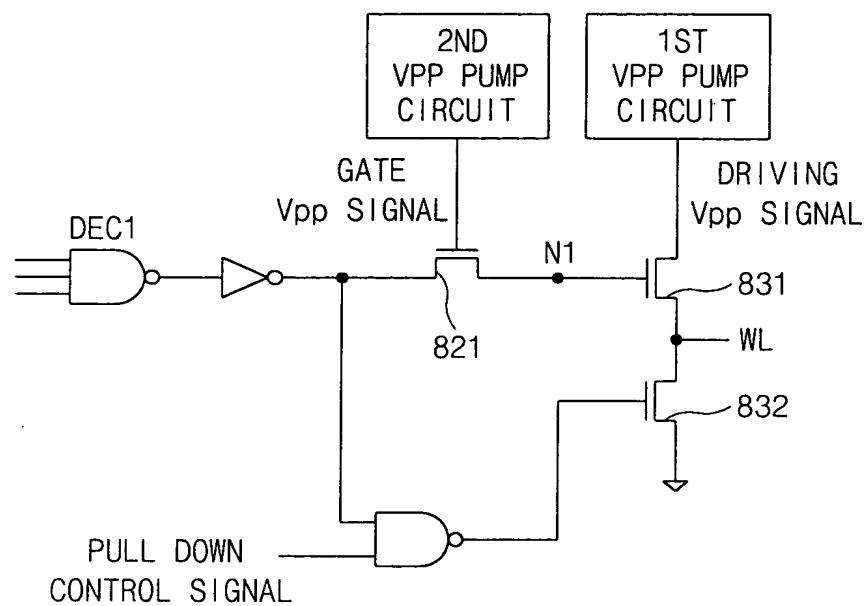


Fig.21

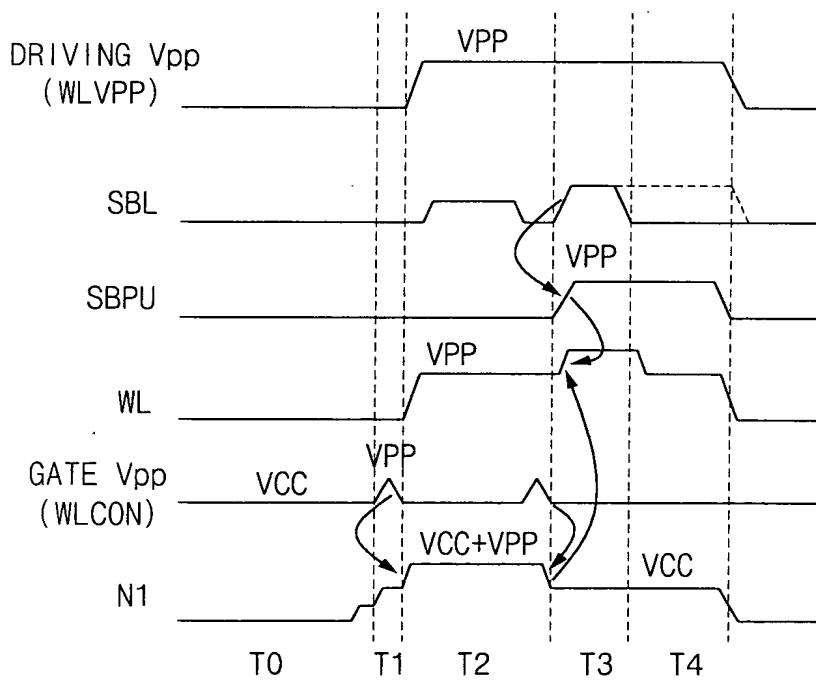


Fig.22